

Abstract**A Semi-Conductor Component with Clock Relaying Device**

The invention involves a component with a connection (3b), as well as at least one further connection (3a), whereby differential input clock pulses (CLK, CLK_T; /CLK, /CLK_T) can be applied to the connections (3a, 3b), or a single input clock pulse (CLK, CLK_T) applied to the connection (3b) and/or to the further connection (3a) –, and where the component in addition has a first and a second pulse relay device (50, 51), where the first pulse relay device (50) has been provided for relaying differential input clock pulses (CLK, CLK_T; /CLK, /CLK_T), and the second pulse relay device (51) for relaying a single input clock pulse (CLK, CLK_T).